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** CONTINUING DATA *****

This application is a DIV of 10/226,290 08/23/2002 PAT 6,753,219
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** FOREIGN APPLICATIONS *****

JAPAN 08-135534 05/29/1996

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 39	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

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TITLE

Method of manufacturing semiconductor integrated circuit devices having a memory device with a reduced bit line stray capacity and such semiconductor integrated circuit devices

<p>FILING FEE RECEIVED 770</p>	<p>FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:</p>	<p><input type="checkbox"/> All Fees</p> <p><input type="checkbox"/> 1.16 Fees (Filing)</p> <p><input type="checkbox"/> 1.17 Fees (Processing Ext. of time)</p> <p><input type="checkbox"/> 1.18 Fees (Issue)</p> <p><input type="checkbox"/> Other _____</p> <p><input type="checkbox"/> Credit</p>
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